



03S0772

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I declare:

that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

SEMICONDUCTOR INTEGRATED CIRCUIT PROVIDED WITH SEMICONDUCTOR MEMORY CIRCUIT HAVING
REDUNDANCY FUNCTION AND METHOD FOR TRANSFERRING ADDRESS DATA

the specification of which is attached hereto unless the following box is checked.

☒ was filed on March 24, 2004 as United States Application
or PCT International Application No. 10/809,308 and
was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

<u>Country</u>	<u>Category</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Priority Claim</u>
Japan	Patent	2003-408587	December 8, 2003	Yes

And I hereby appoint Barry N. Young (Reg. No. 27,774), Timothy W. Lohse (Reg. No. 35,255), David F. Keinsmith (Reg. No. 40,050), Stephen E. Reiter (Reg. No. 31,192), Wayne P. Sobon (Reg. No. 32,438), Robroy R. Fawcett (Reg. No. 35,133), Steven R. Sprinkle (Reg. No. 40,825), William N. Hulsey III (Reg. No. 33,402), Terrance A. Meador (Reg. No. 30,298), John Schlicher (Reg. No. 28,343), June M. Learn (Reg. No. 31,238), John Oskorep (Reg. No. 41,234), Timothy N. Ellis (Reg. No. 41,734), David R. Stevens (Reg. No. 38,626), William G. Goldman (Reg. No. 42,590), Derek Westberg (Reg. No. 40,872), Alan A. Limbach (Reg. No. 39,749), Edward B. Weller (Reg. No. 37,468), Ronald L. Yin (Reg. No. 27,607), and George C. Limbach (Reg. No. 19,305) each of whose address is 2000 University Circle, East Palo Alto, CA 94303-2248, or any one of them, my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent & Trademark Office connected therewith, and request that correspondence be directed to Gray Cary Ware & Freidenrich L.L.P., 2000 University Circle, East Palo Alto, CA 94303-2248.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION FOR PATENT APPLICATION

I declare further that my mailing address is at c/o
 Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura
 1-chome, Minato-ku, Tokyo 105-8001, Japan; and
 that my citizenship and residence are as stated below next to my name:

Inventor: (Signature)

Date

Residence

Date: MAY. 20. 2004

Takehiko Hojo

Takehiko Hojo

Citizen of: Japan

Yokohama-shi, Japan

Date: MAY. 20. 2004

Akikuni Sato

Akikuni Sato

Citizen of: Japan

Yokohama-shi, Japan

Date:

Citizen of: Japan

Date:

Citizen of: Japan

Date:

Citizen of: Japan

Date:

Citizen of: Japan

Date:

Citizen of: Japan

Date:

Citizen of: Japan

